

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	8298515
<b>Application Number:</b>	10773434
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	6596
<b>Title of Invention:</b>	ELECTROPLATED INTERCONNECTION STRUCTURES ON INTEGRATED CIRCUIT CHIPS
<b>First Named Inventor/Applicant Name:</b>	Panayotis Constantinou Andricacos
<b>Customer Number:</b>	47939
<b>Filer:</b>	Wenjie Li/Carol Riedinger
<b>Filer Authorized By:</b>	Wenjie Li
<b>Attorney Docket Number:</b>	YOR91995137X
<b>Receipt Date:</b>	31-AUG-2010
<b>Filing Date:</b>	09-FEB-2004
<b>Time Stamp:</b>	14:43:00
<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$100
RAM confirmation Number	1073
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The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

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<b>File Listing:</b>					
<b>Document Number</b>	<b>Document Description</b>	<b>File Name</b>	<b>File Size(Bytes)/Message Digest</b>	<b>Multi Part/.zip</b>	<b>Pages (if appl.)</b>
1	Miscellaneous Incoming Letter	YOR919950137USS_Request_Letter_For_Certificate_Correction.pdf	74108 ee2ab1f9679e01ce99017e09ebd029e13a4880d2d	no	3
<b>Warnings:</b>					
<b>Information:</b>					
2	Request for Certificate of Correction	YOR919950137USS_Certificate_of_Correction.pdf	108610 b62ab653d9fc1c79d300ce93987ef72f931cb5173	no	2
<b>Warnings:</b>					
<b>Information:</b>					
3	Fee Worksheet (PTO-875)	fee-info.pdf	30068 05-39ab92-1cc3b3f93d387612417be02a98fb-00f	no	2
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			212786		
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